

11000 U.S. PTO

12/26/96

		Subclass	ISSUE CLASSIFICATION
		Class	

Best Available Copy

PATENT NUMBER

U.S. UTILITY Patent Application

 O.I.P.E. SCANNED <u>TD2 o.a Am</u>	PATENT DATE
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APPLICATION NO. 09/912721	CONT/PRIOR D	CLASS 455	SUBCLASS 570	ART UNIT 2684	EXAMINER J Lee
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Paul Chou
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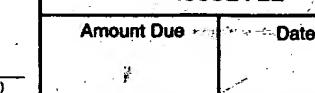
Distributed micro instruction set processor architecture for high-efficiency signal processing

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED			
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED 			
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____					ISSUE FEE <table border="1"> <tr> <td>Amount Due</td> <td>Date Paid</td> </tr> </table>		Amount Due	Date Paid
Amount Due	Date Paid							
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.					ISSUE BATCH NUMBER 			
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